



TITLE: EOS PROTECTION STRUCTURE FOR INTEGRATED  
CIRCUIT DEVICES (AS AMENDED)  
Inventor: Mark McQueen  
Serial No.: 09/146,851  
Docket No.: 2269-3522.2US

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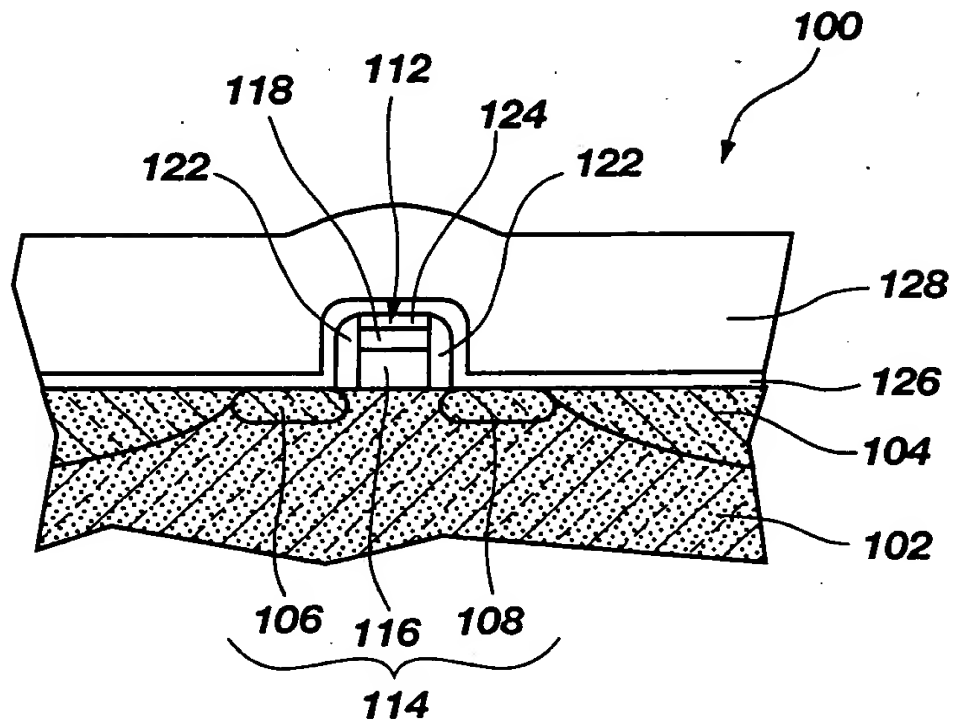


Fig. 1

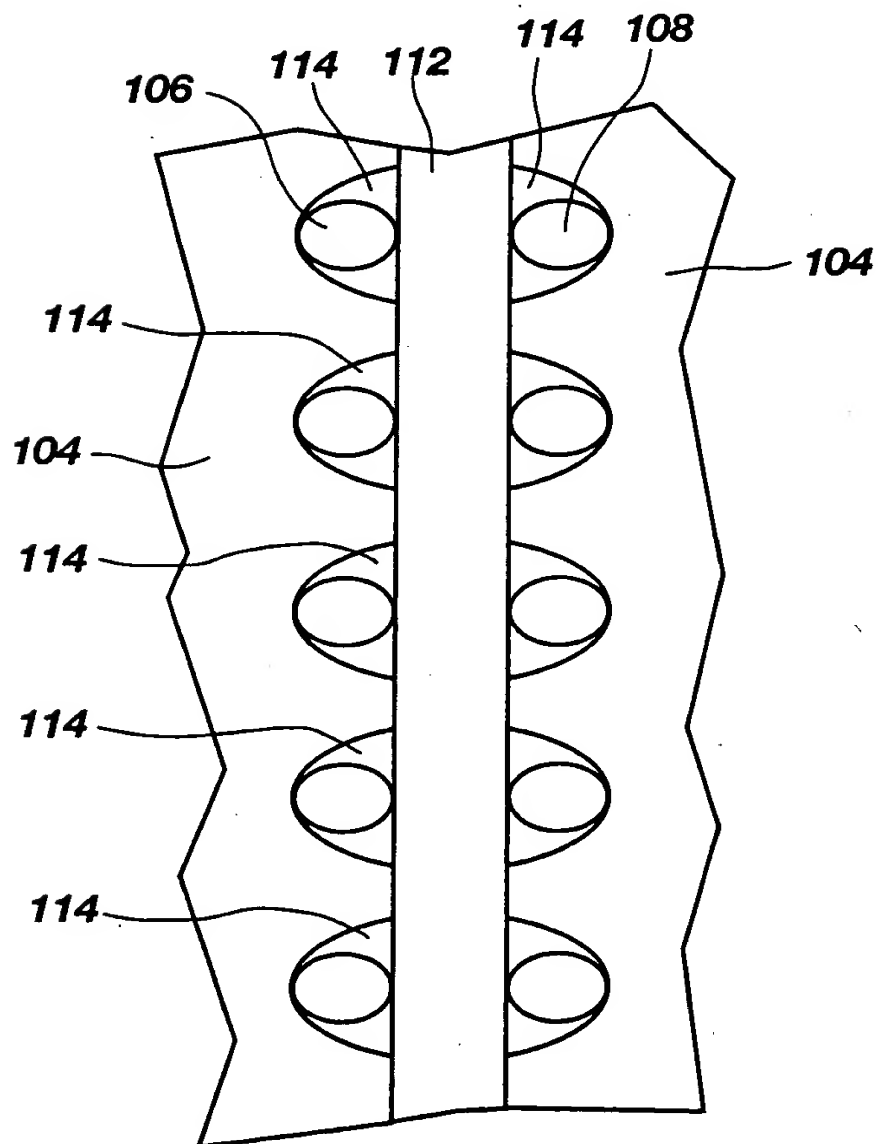
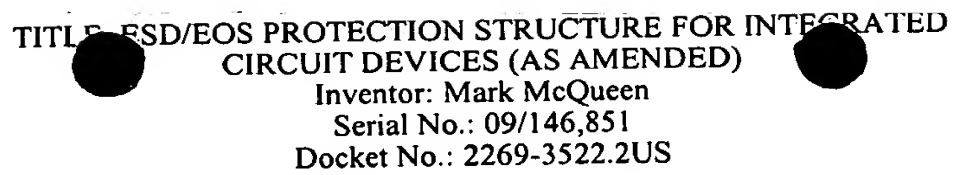


Fig. 2

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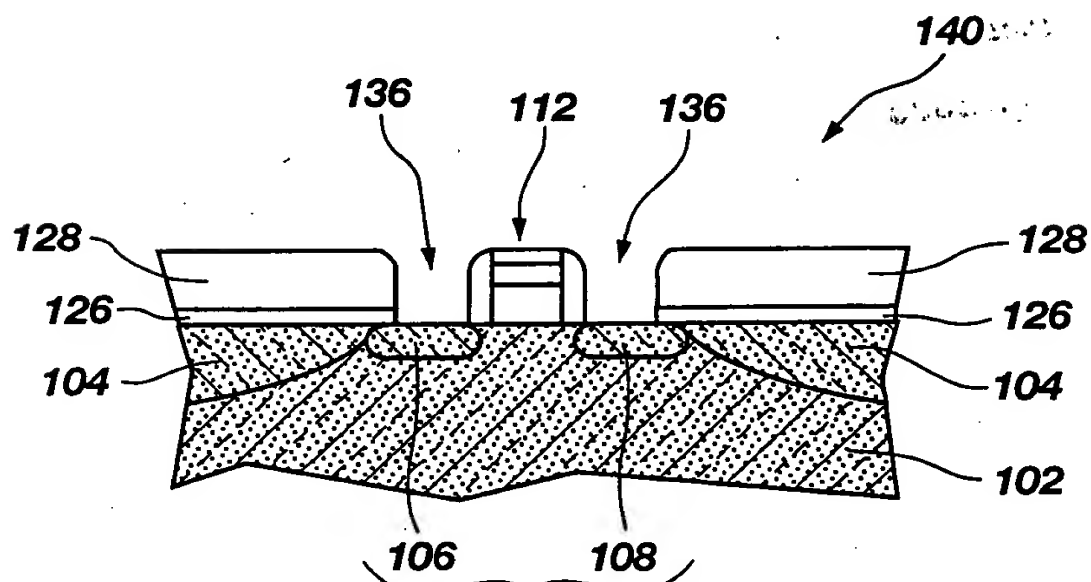


Fig. 6

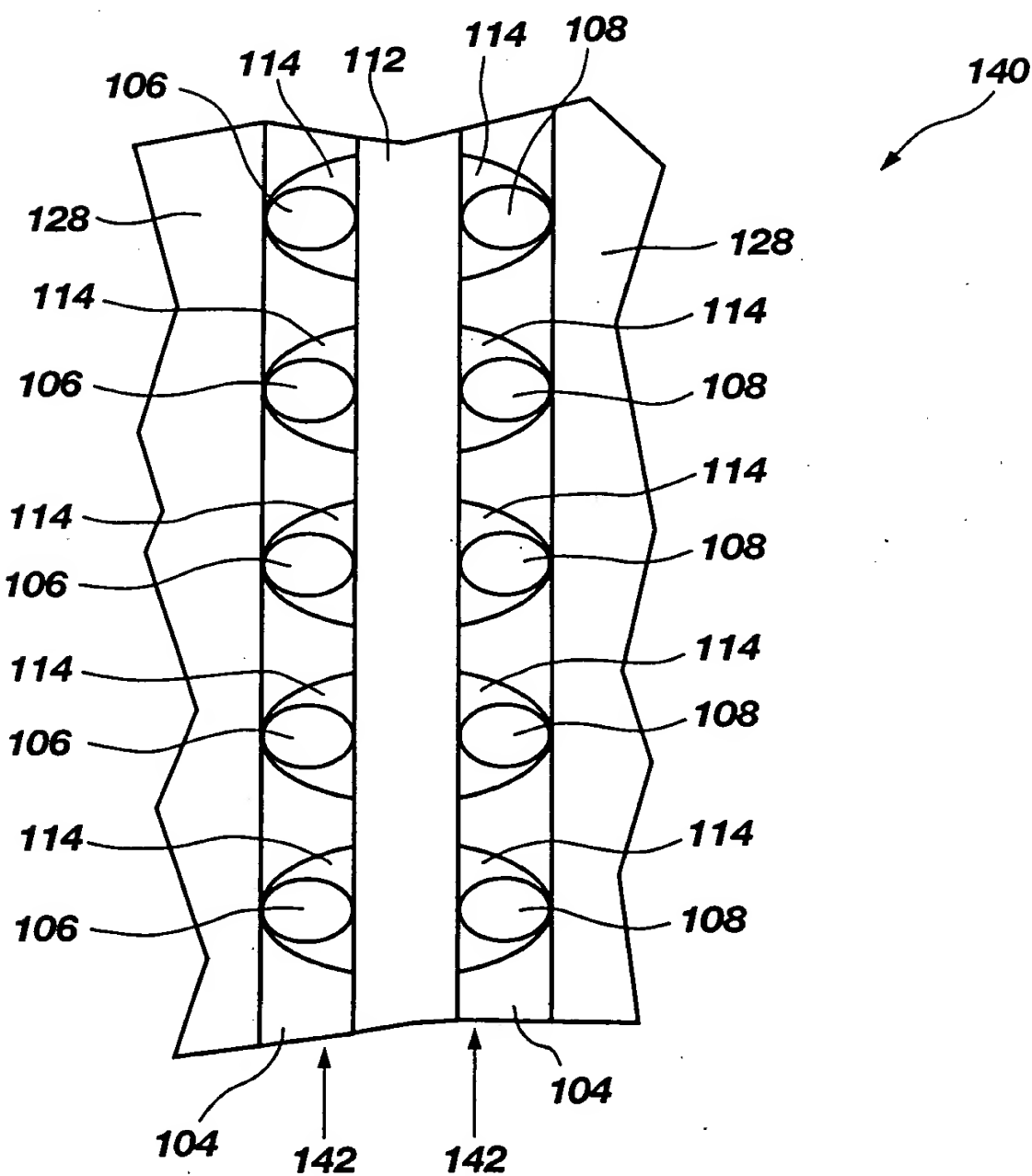
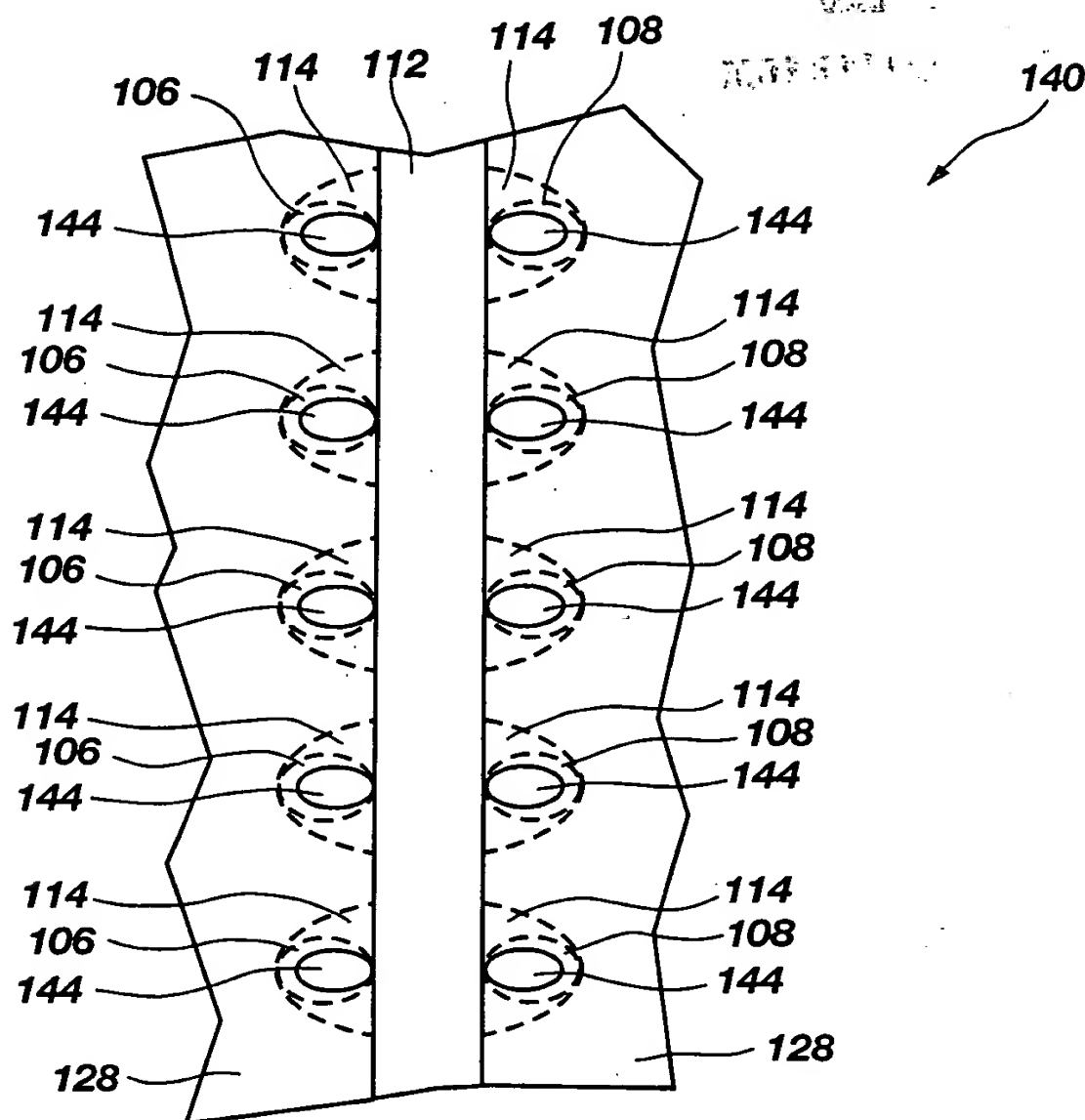


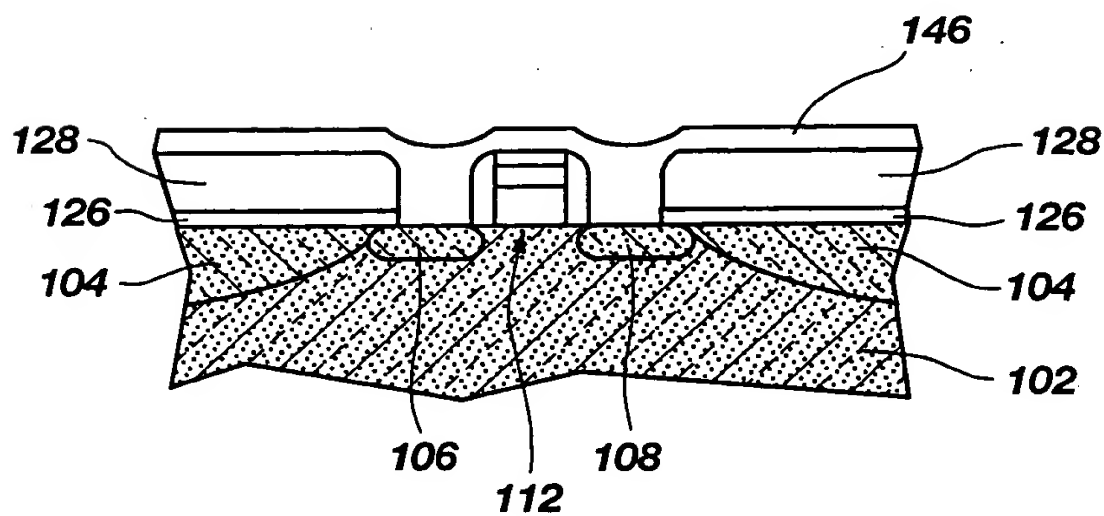
Fig. 7



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**Fig. 8**



**Fig. 9**



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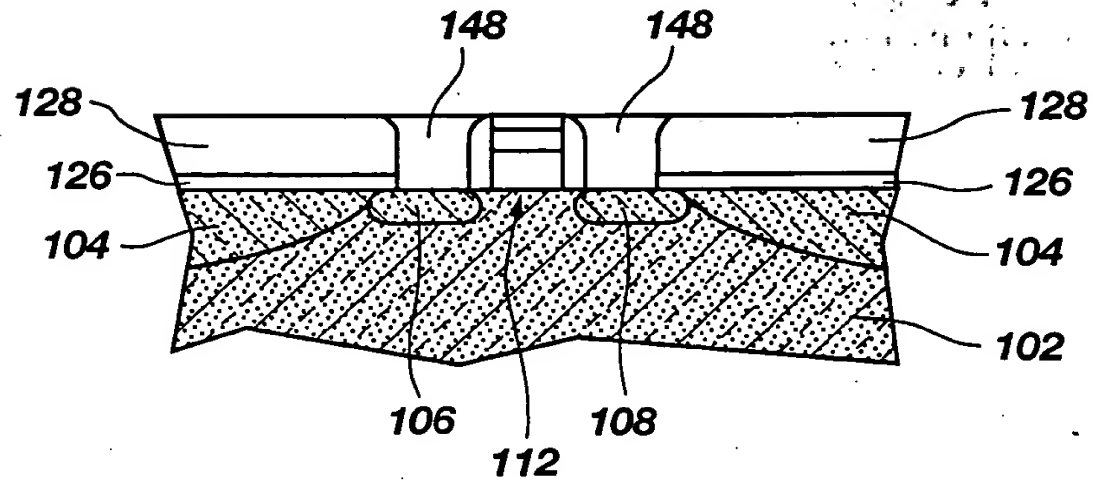


Fig. 10

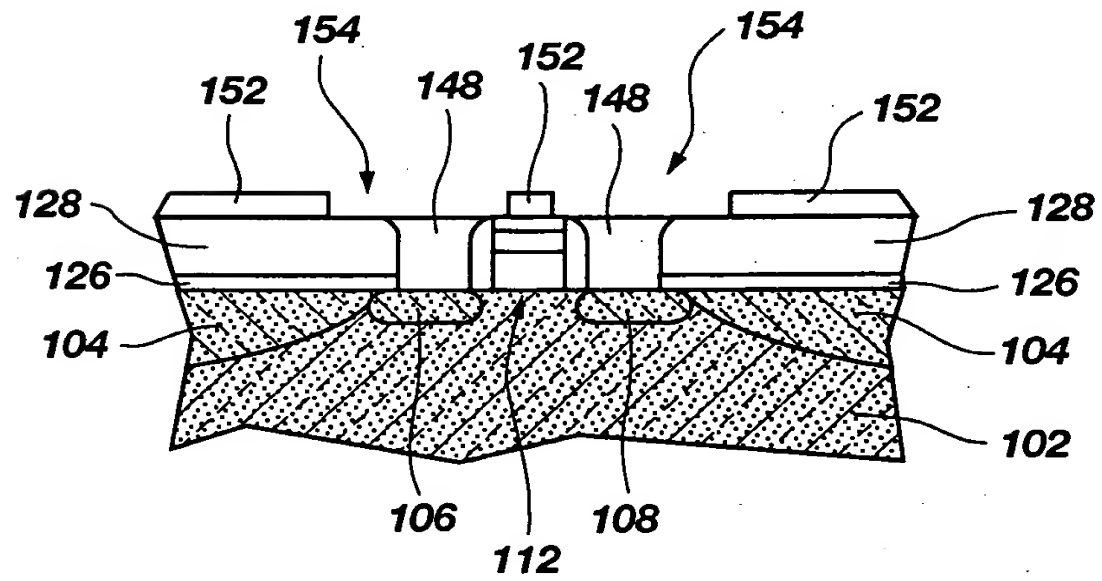


Fig. 11

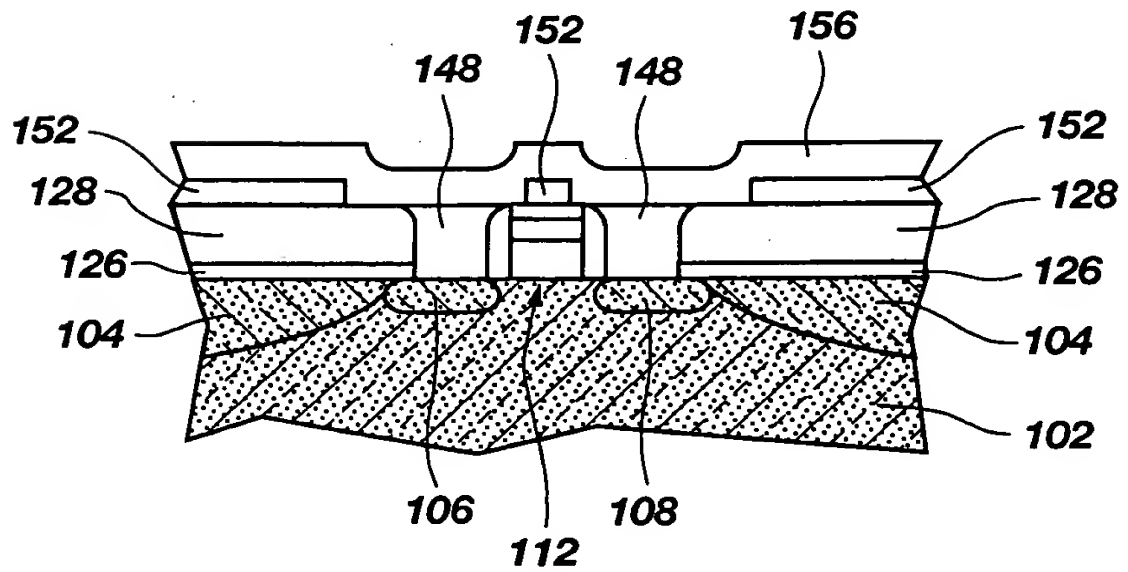


Fig. 12



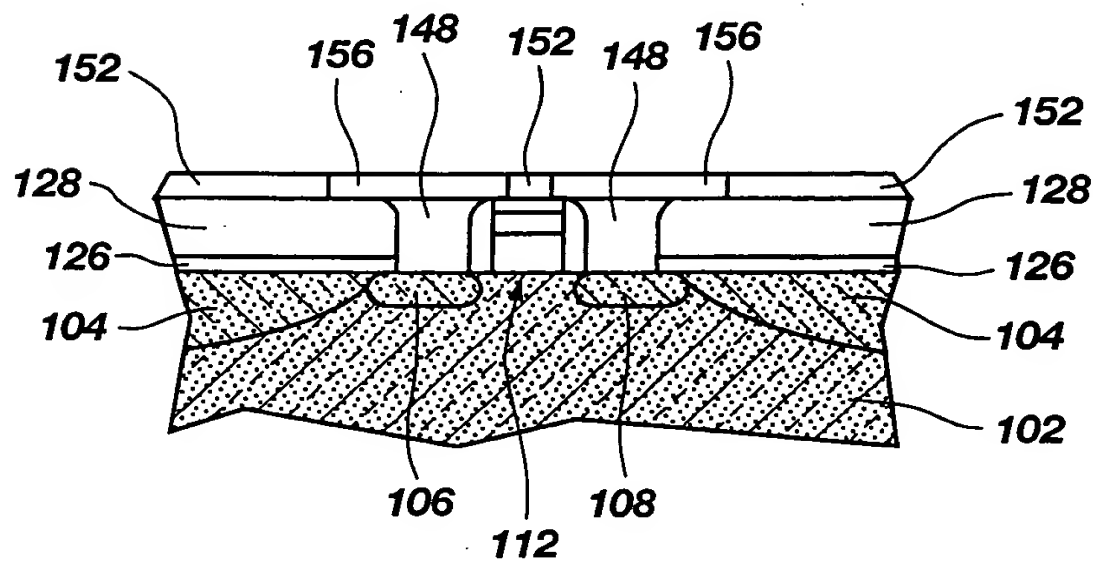
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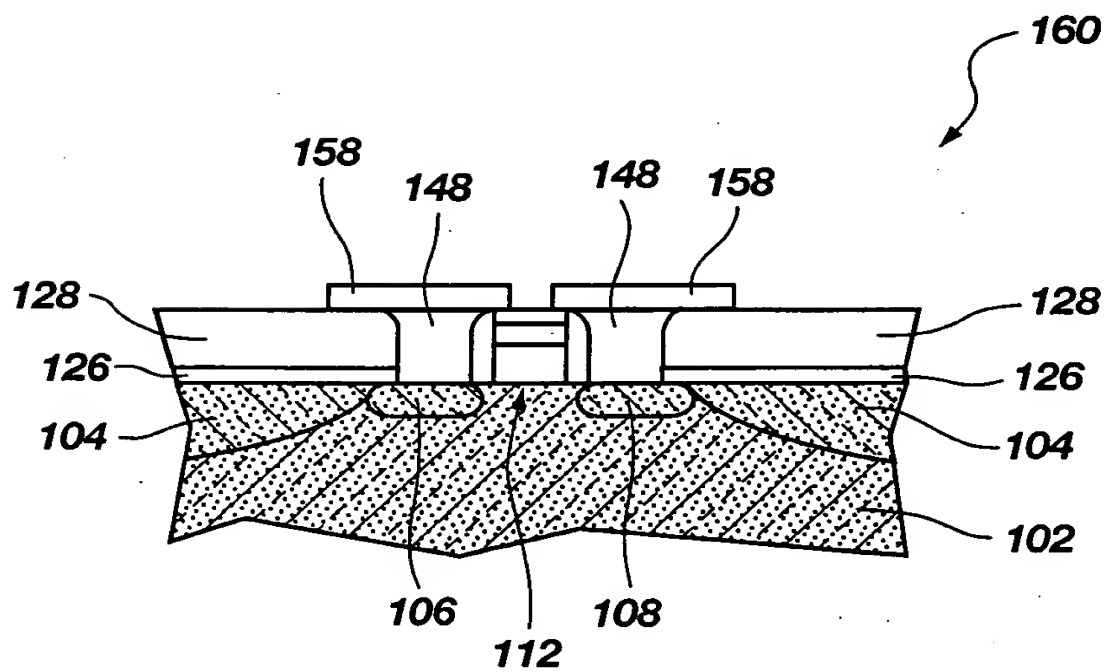
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**Fig. 13**



**Fig. 14**



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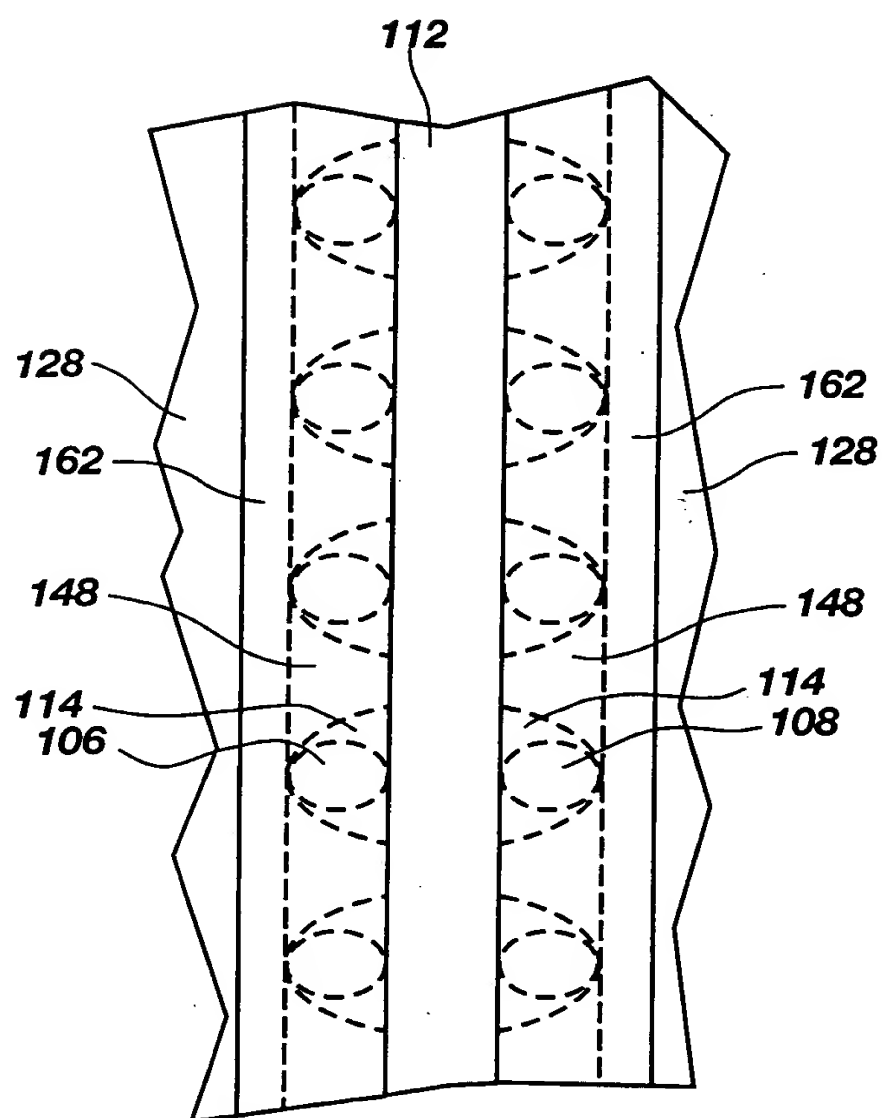


Fig. 15

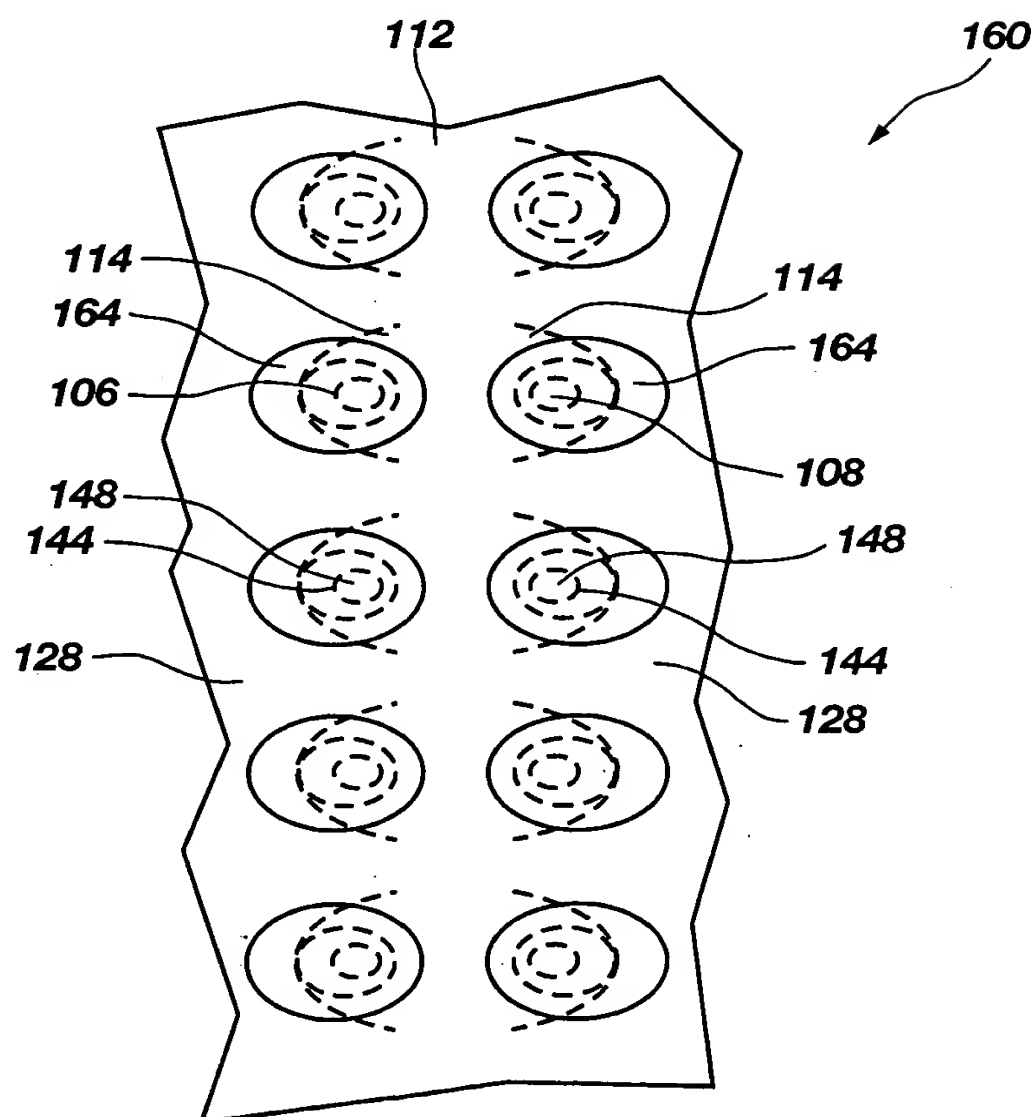


Fig. 16

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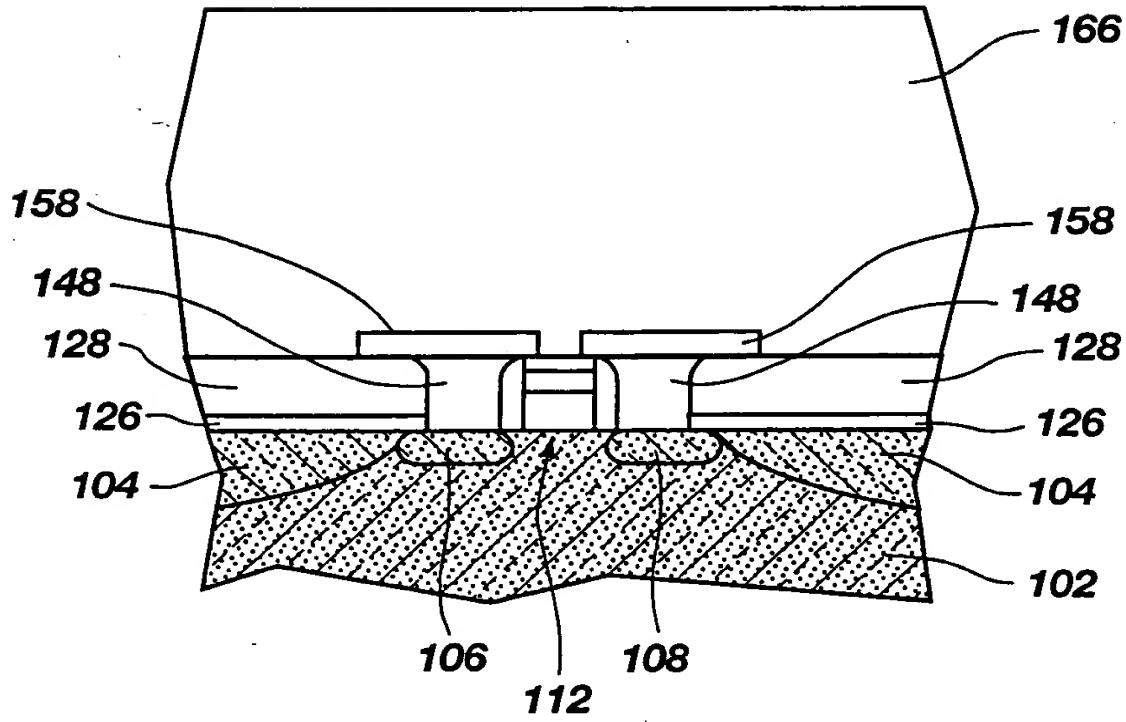


Fig. 17

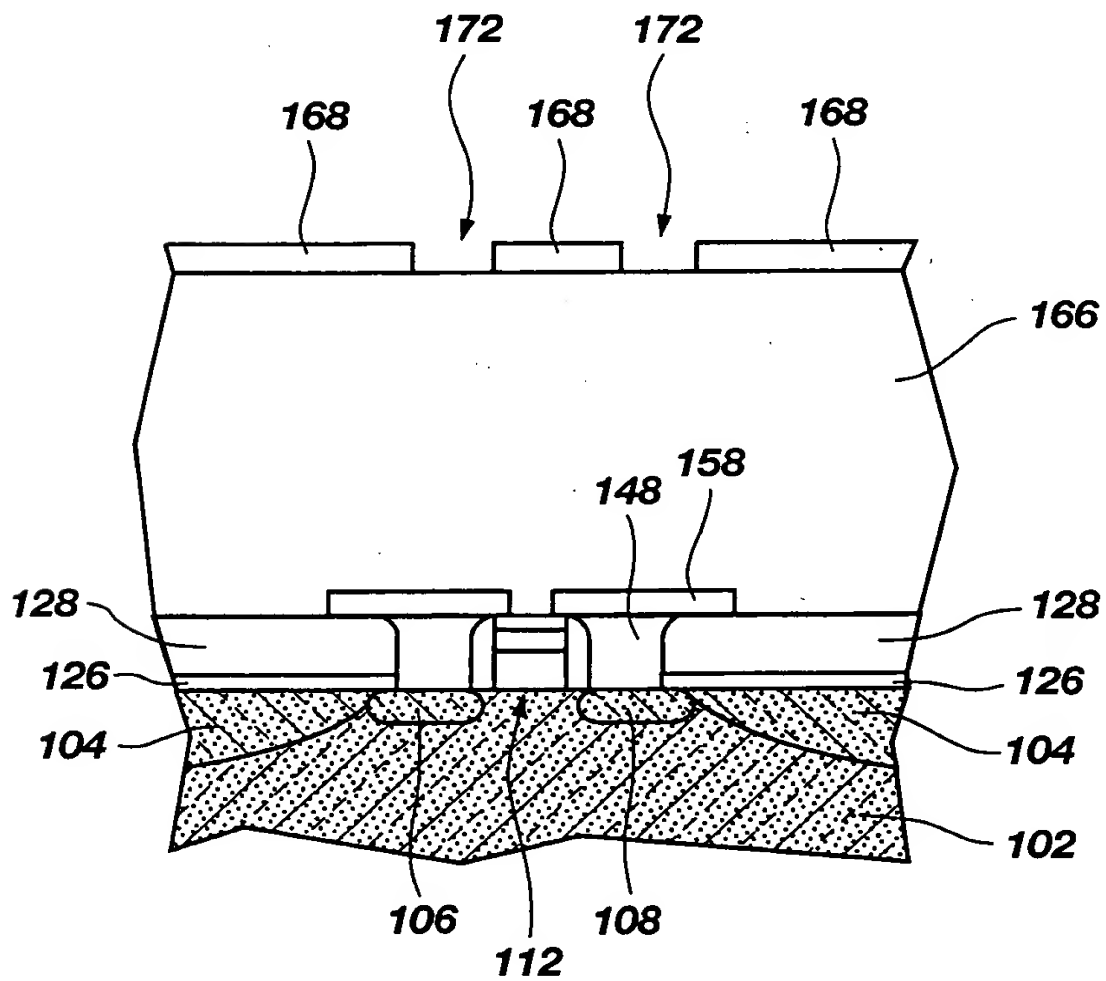


Fig. 18



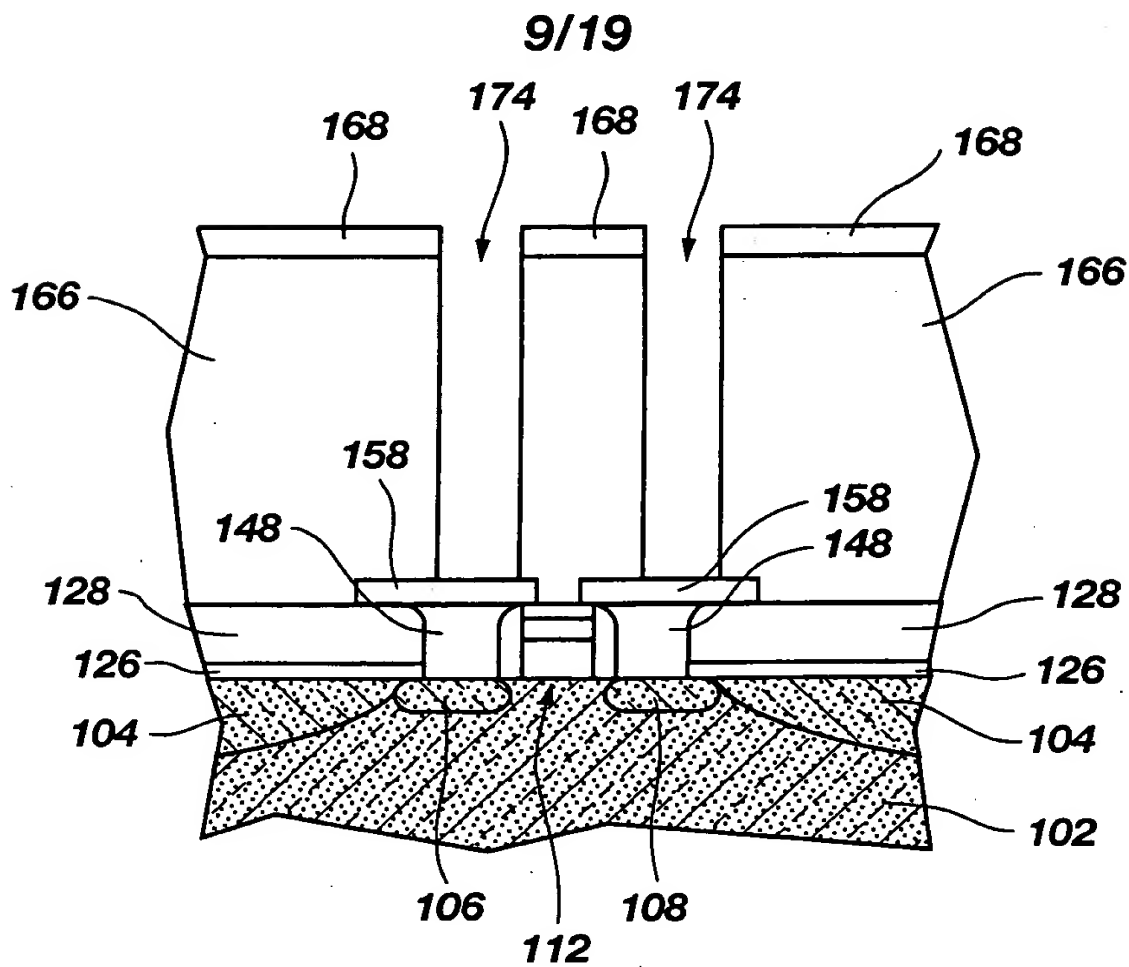


Fig. 19

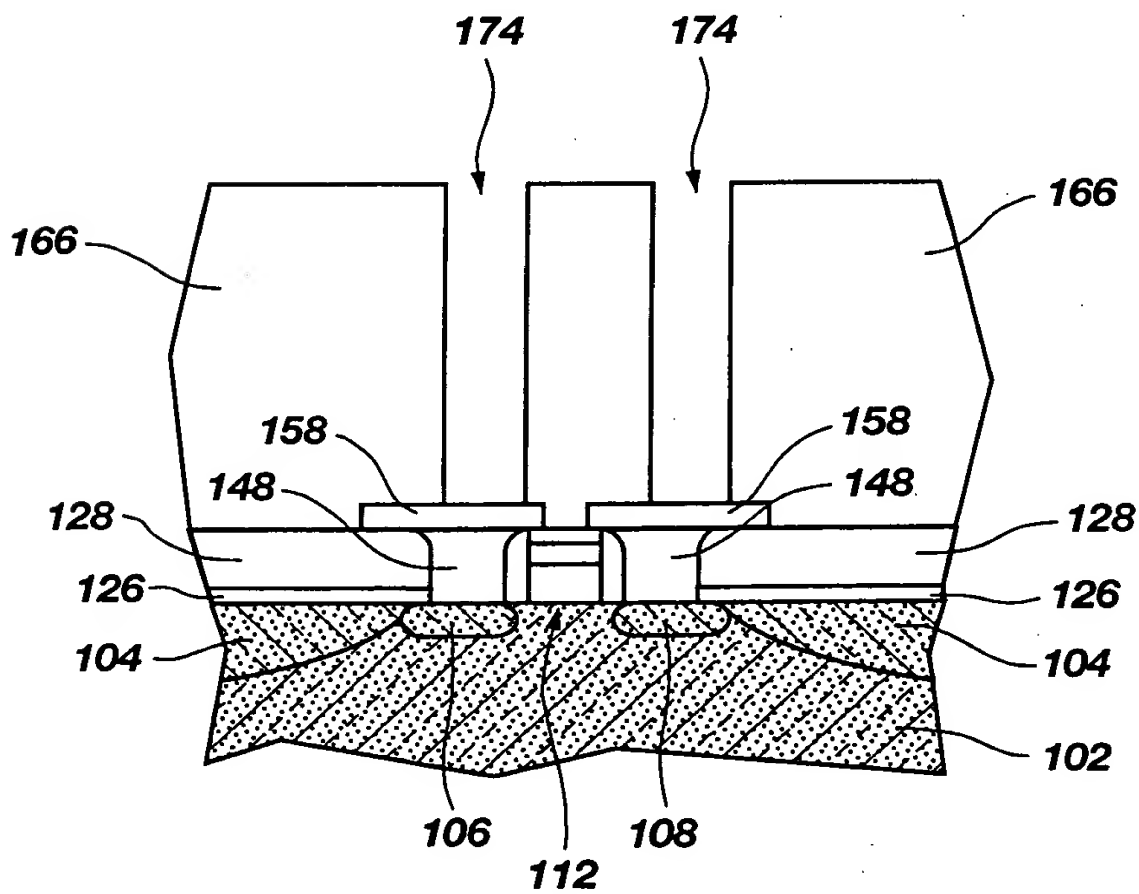


Fig. 20



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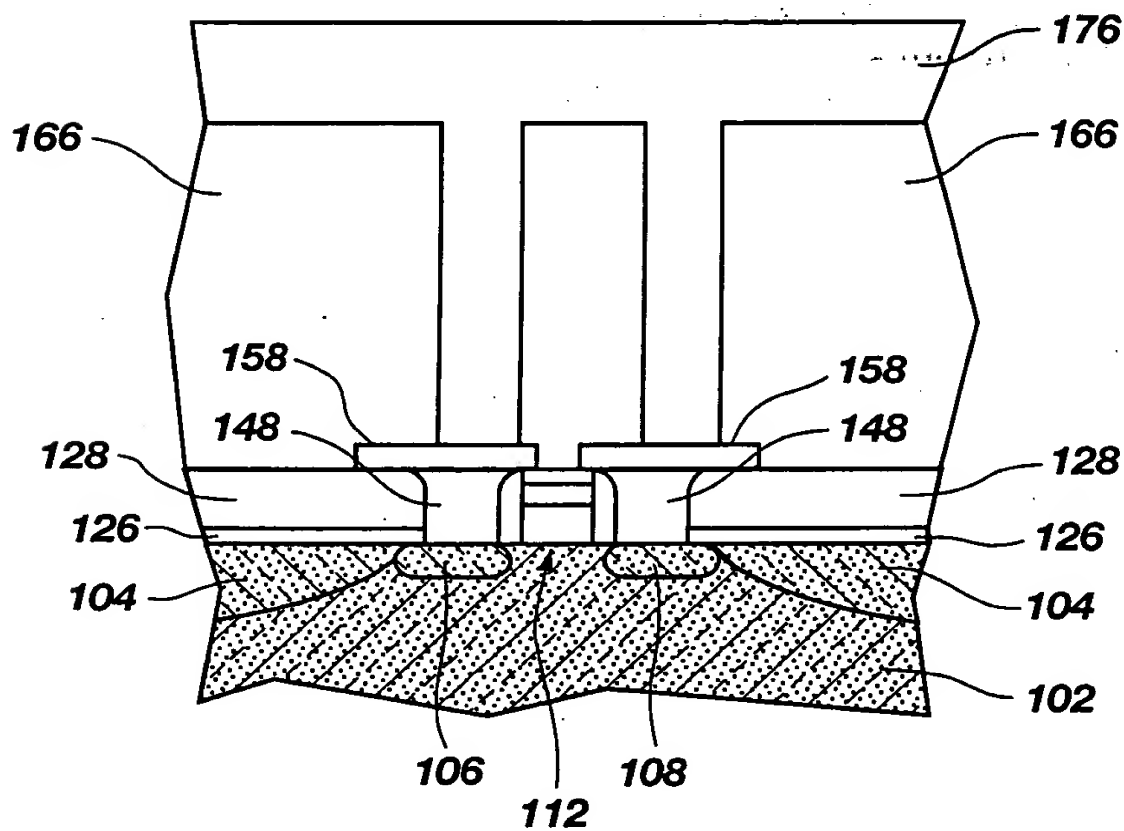


Fig. 21

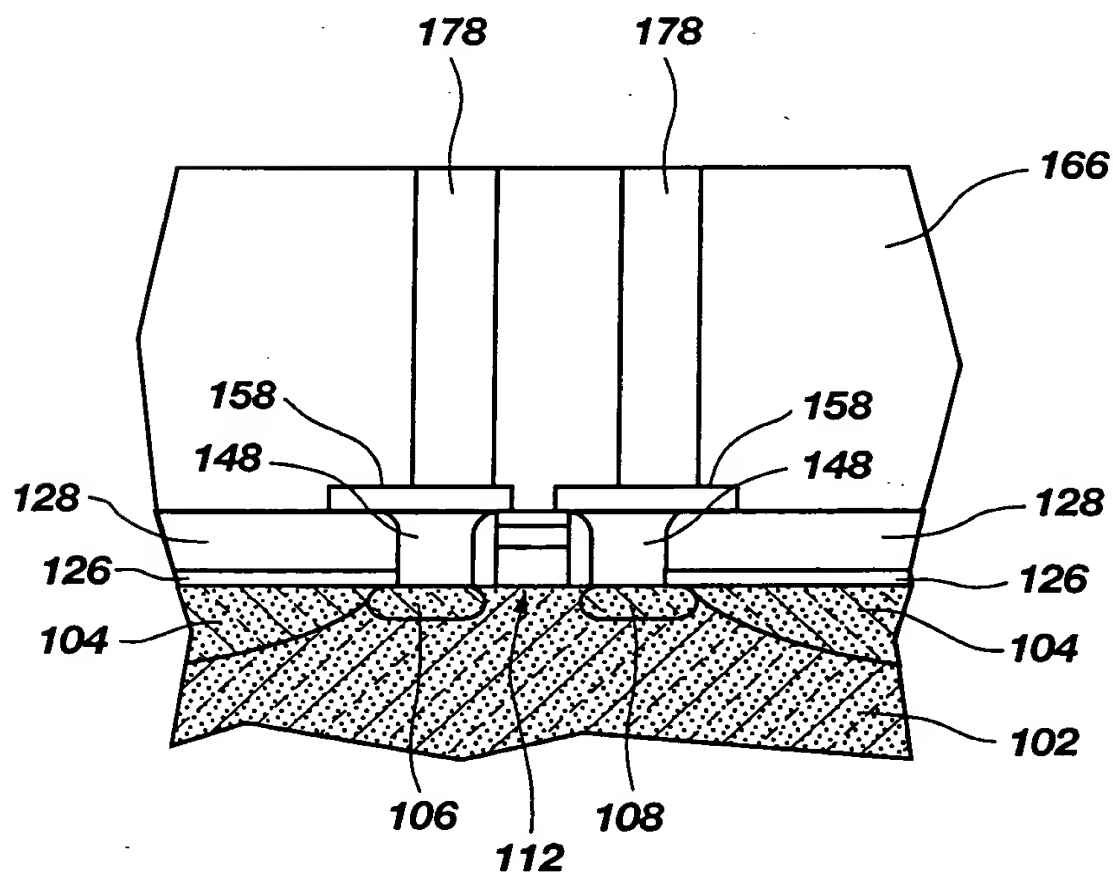


Fig. 22



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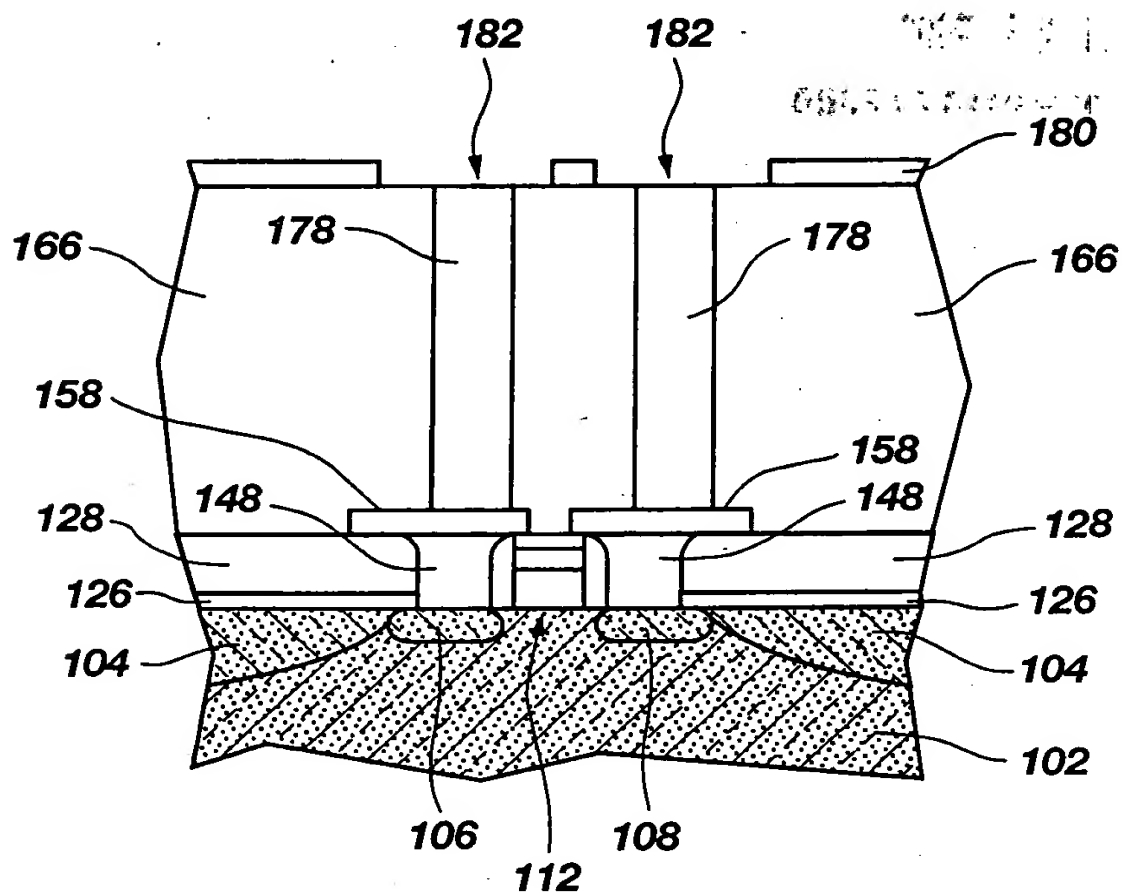


Fig. 23

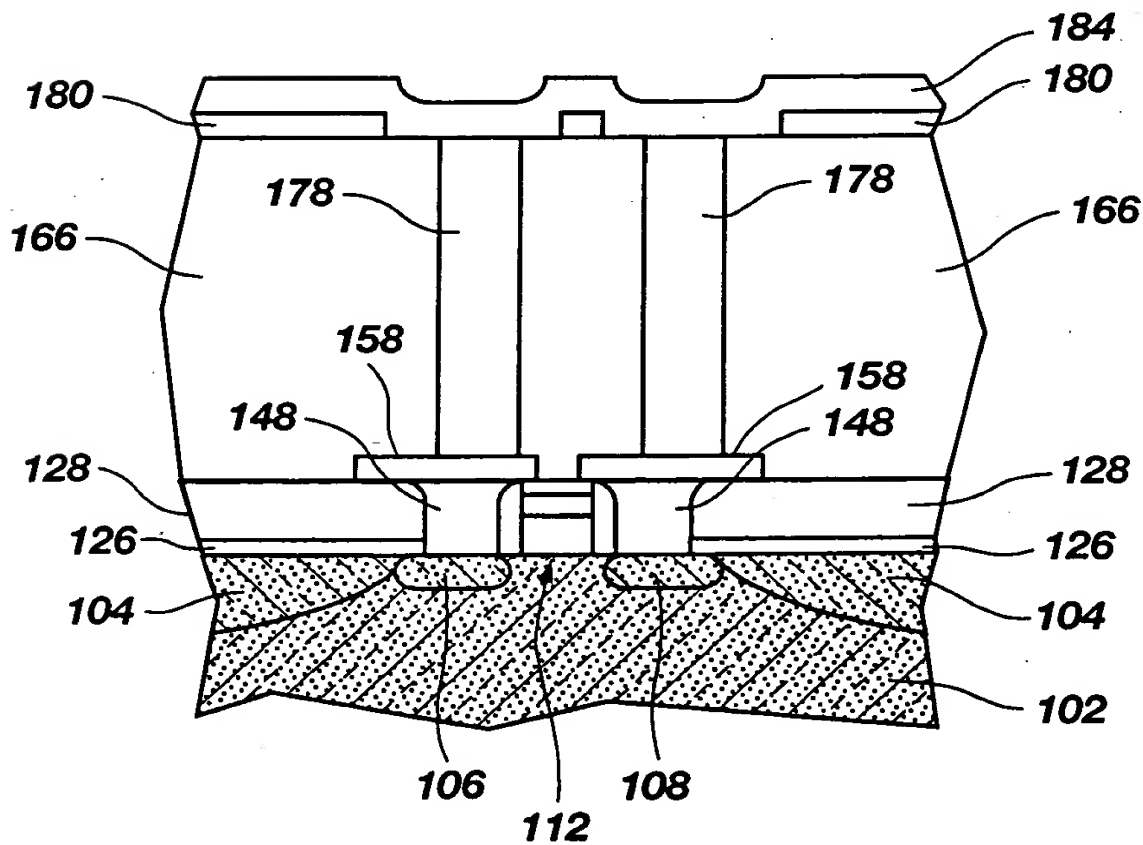


Fig. 24



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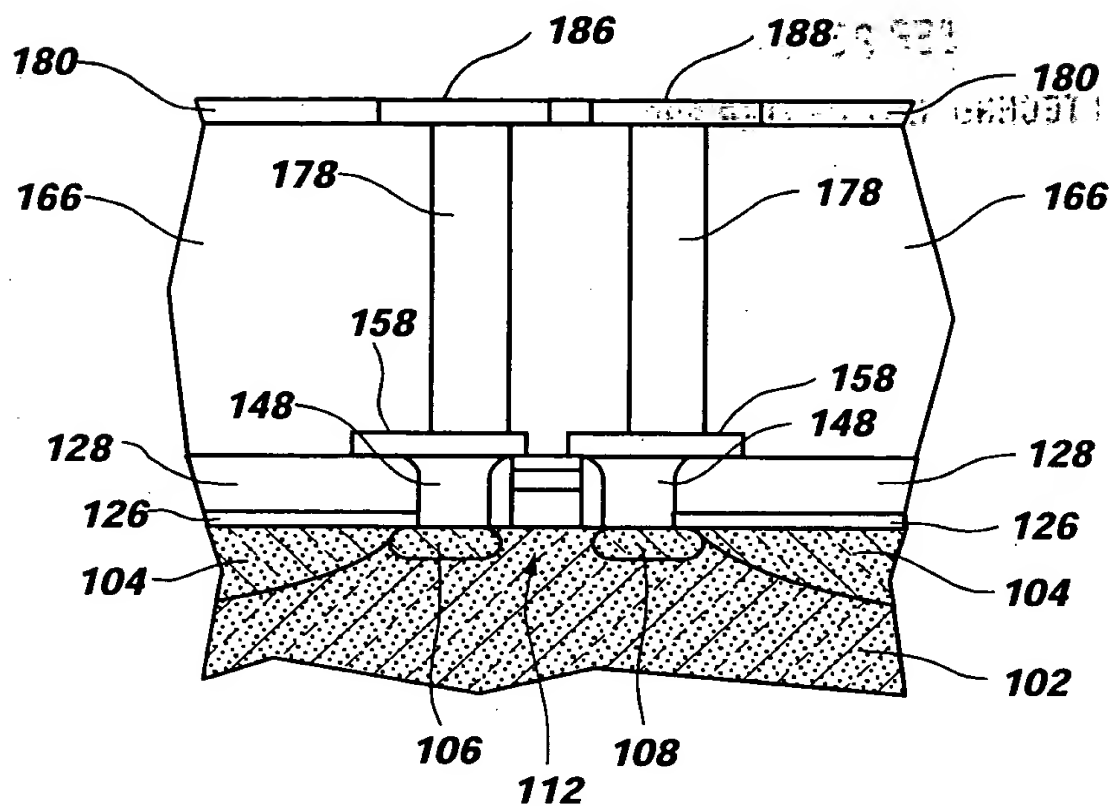


Fig. 25

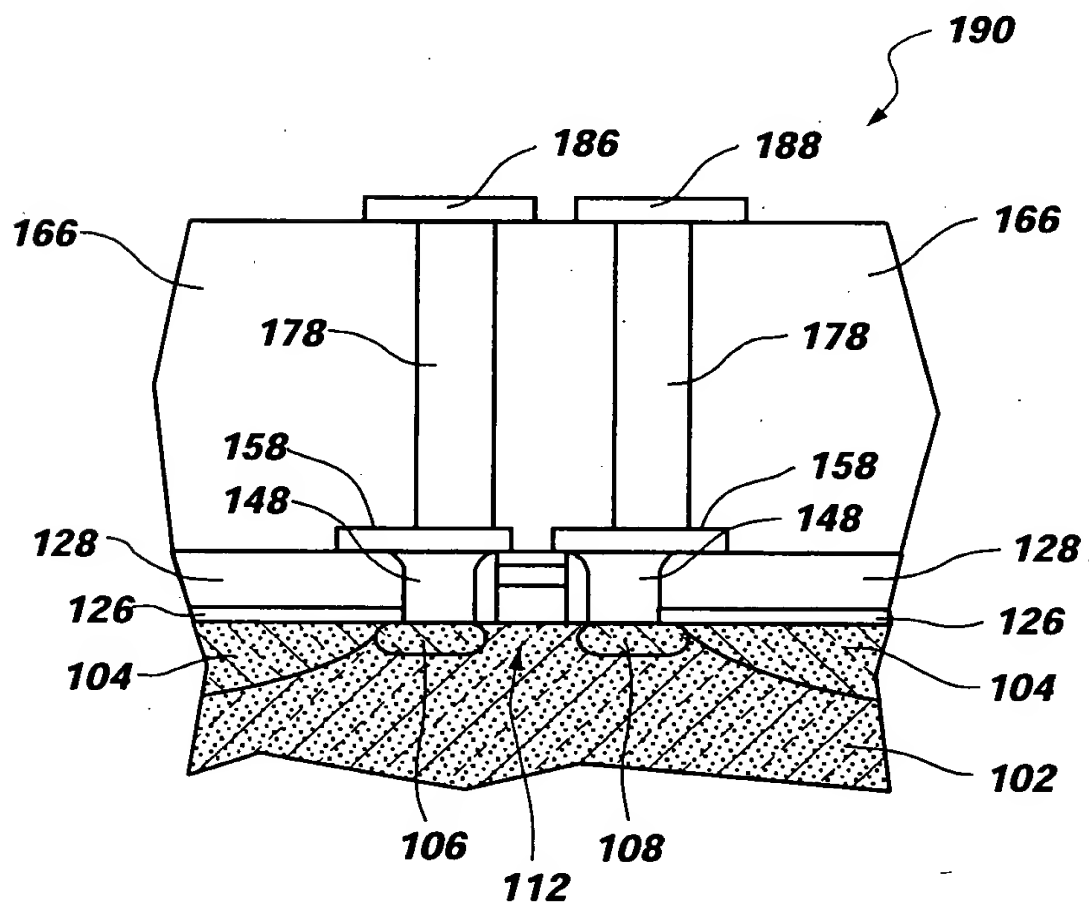


Fig. 26



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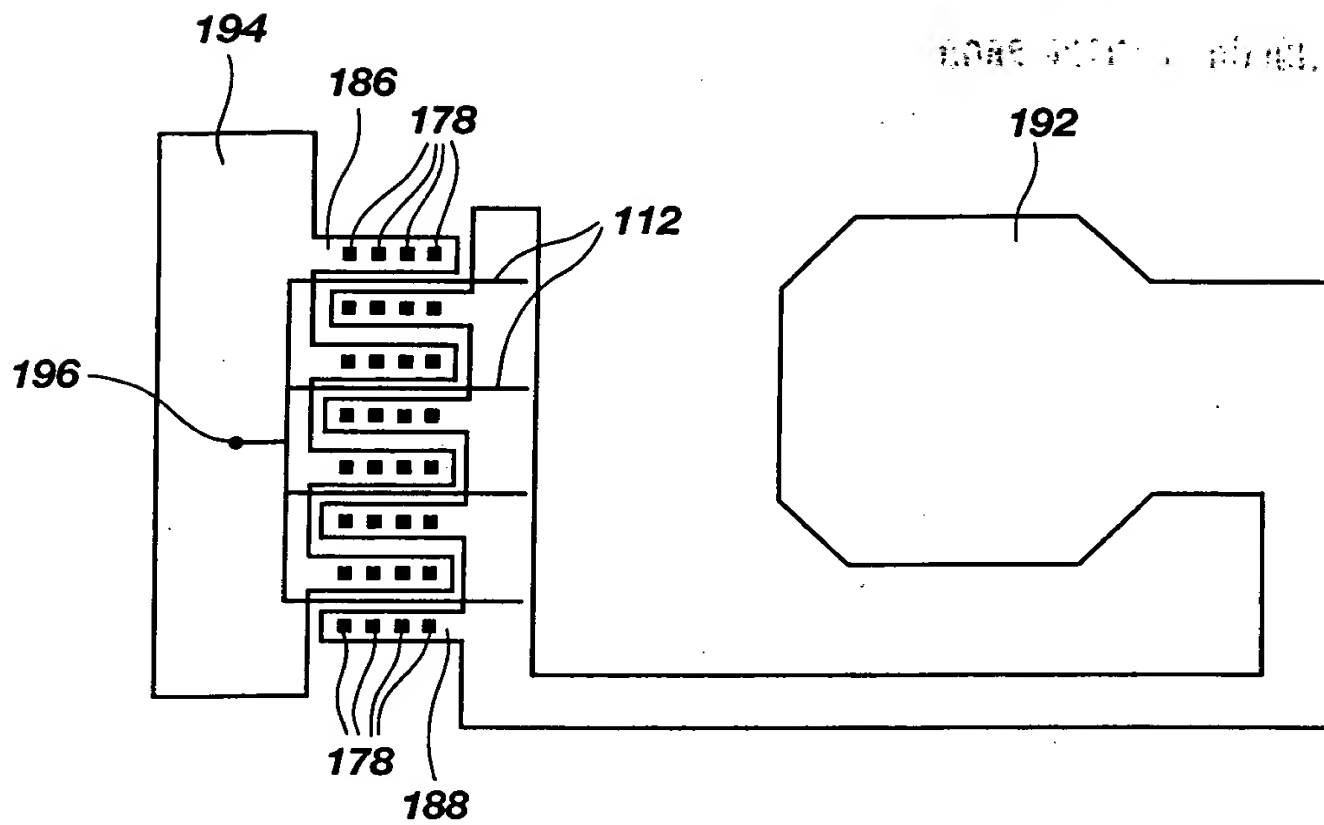


Fig. 27

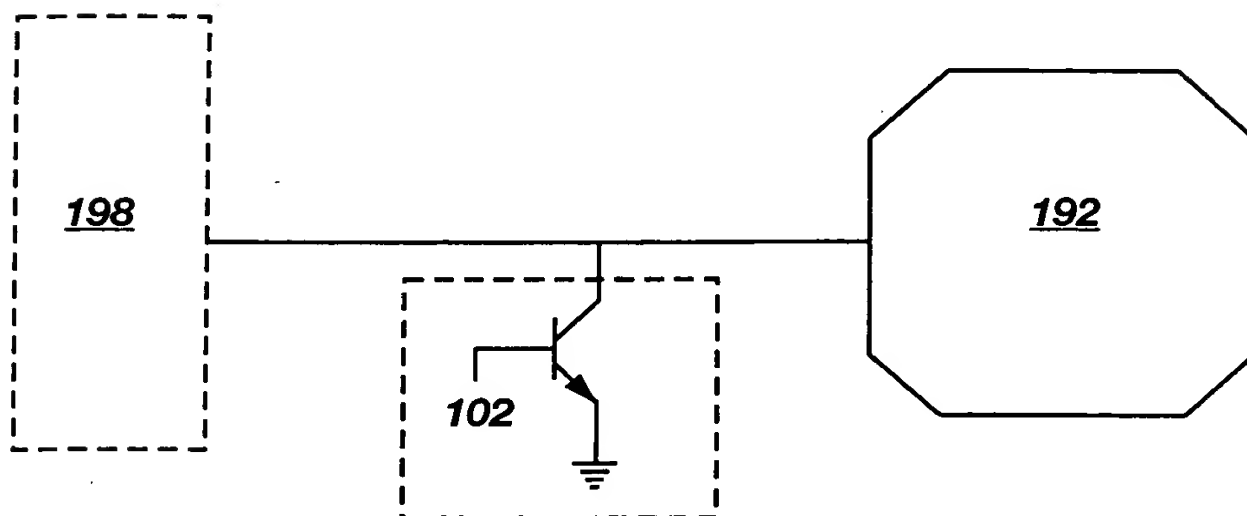


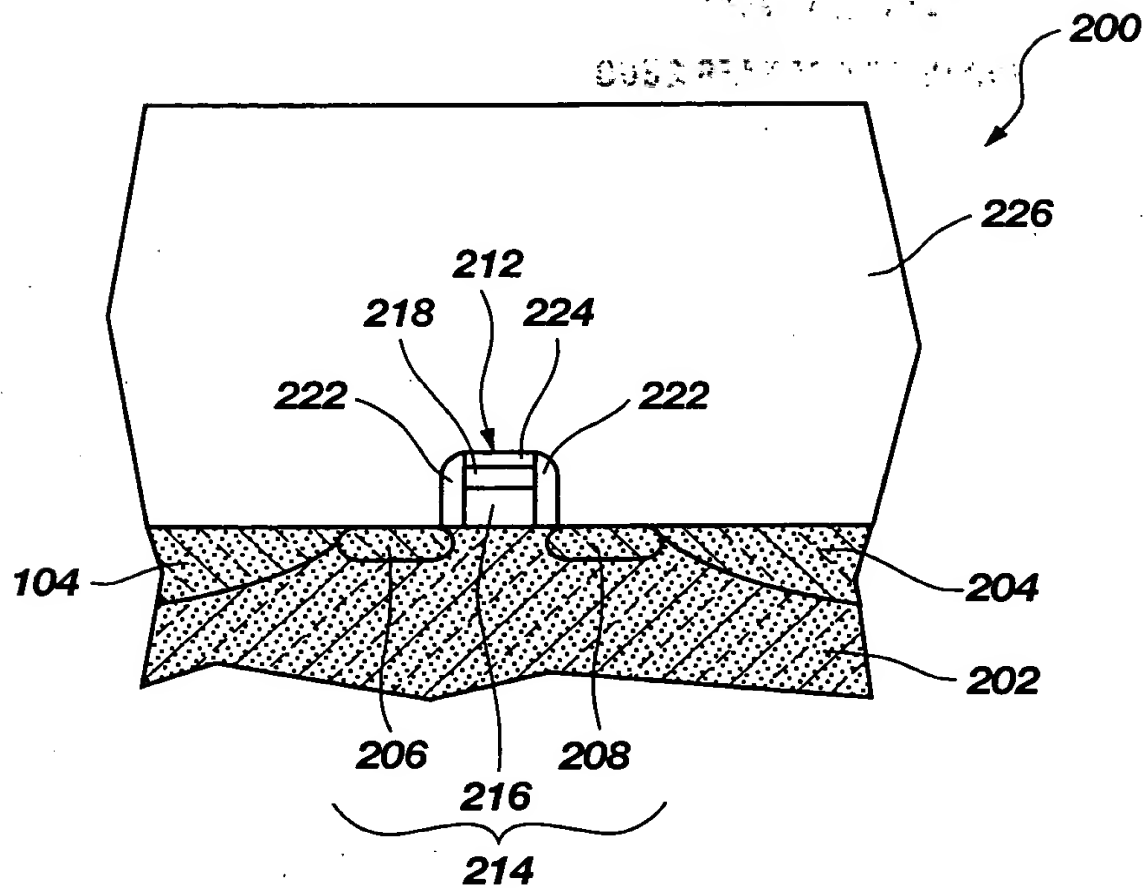
Fig. 28



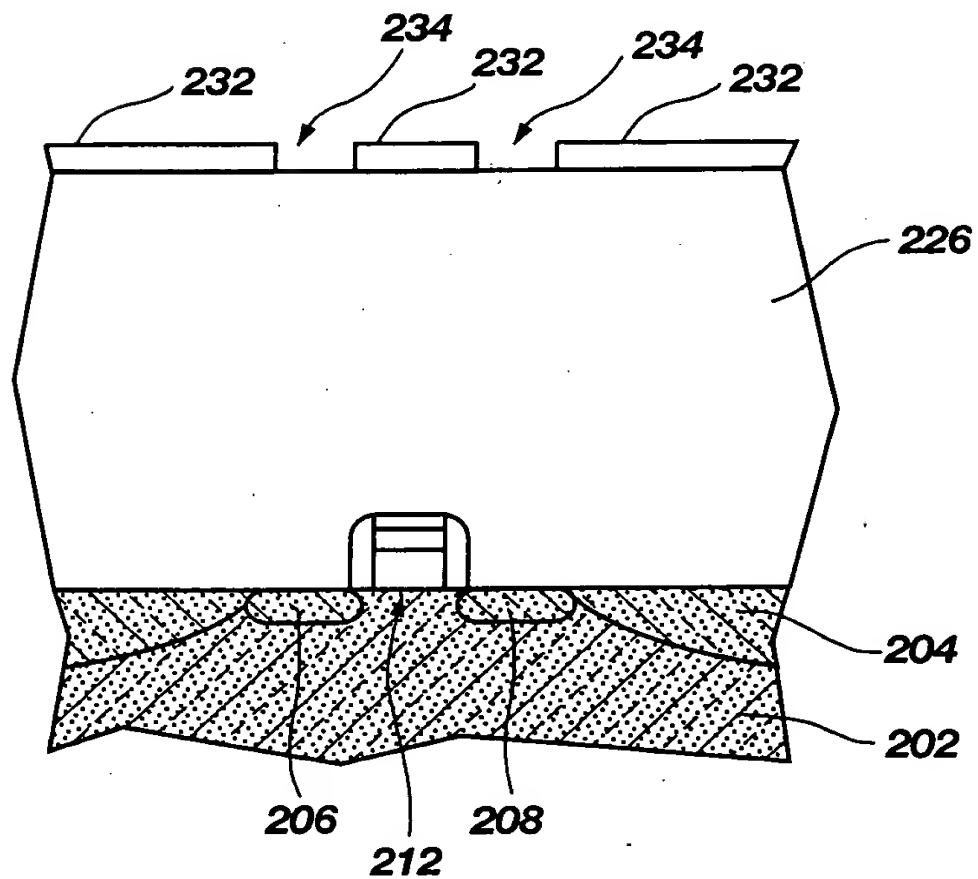
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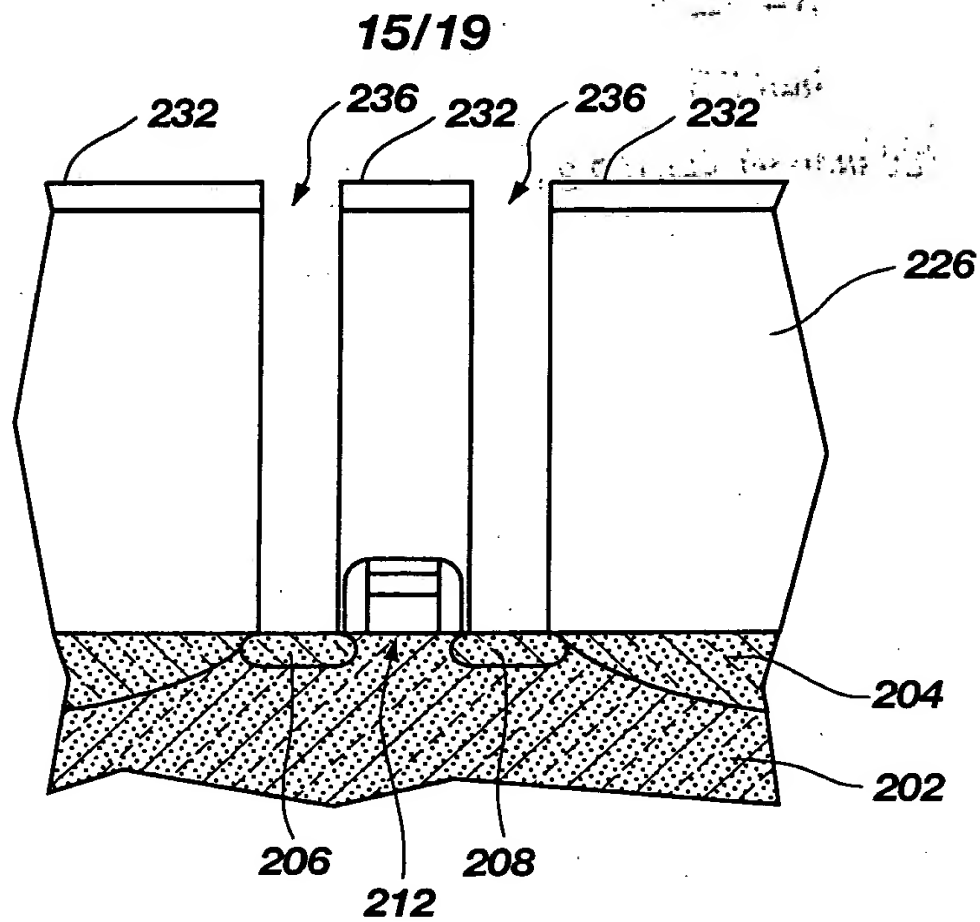
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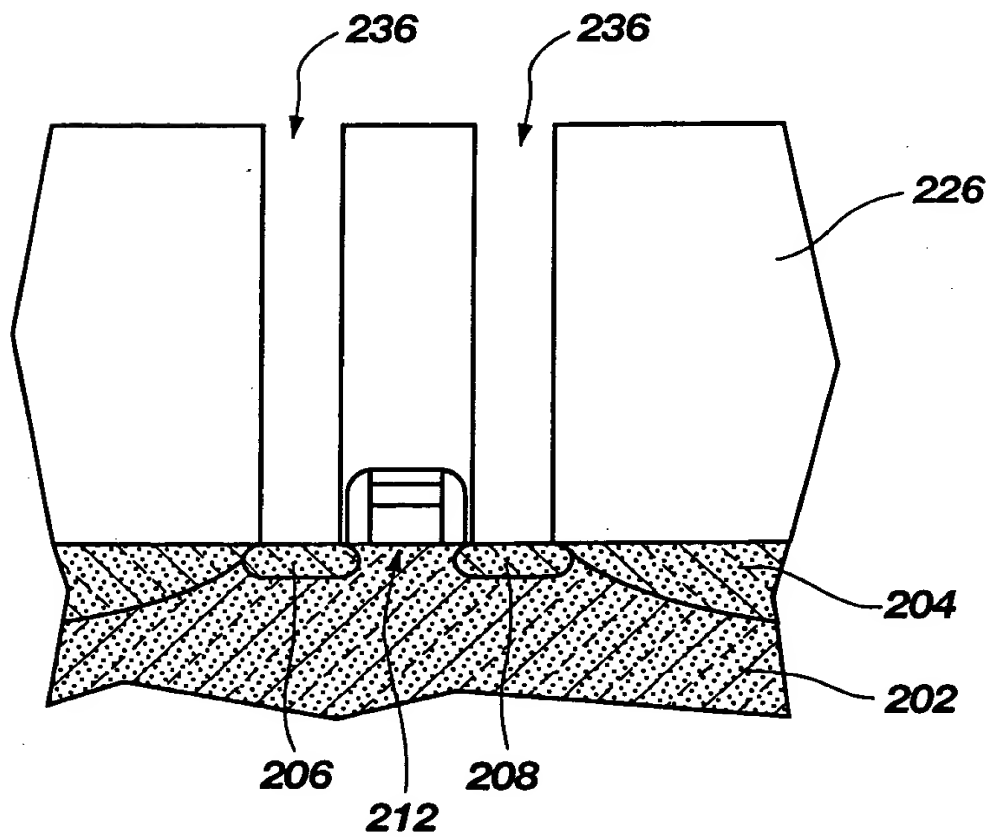
**Fig. 29**  
**(PRIOR ART)**



**Fig. 30**  
**(PRIOR ART)**



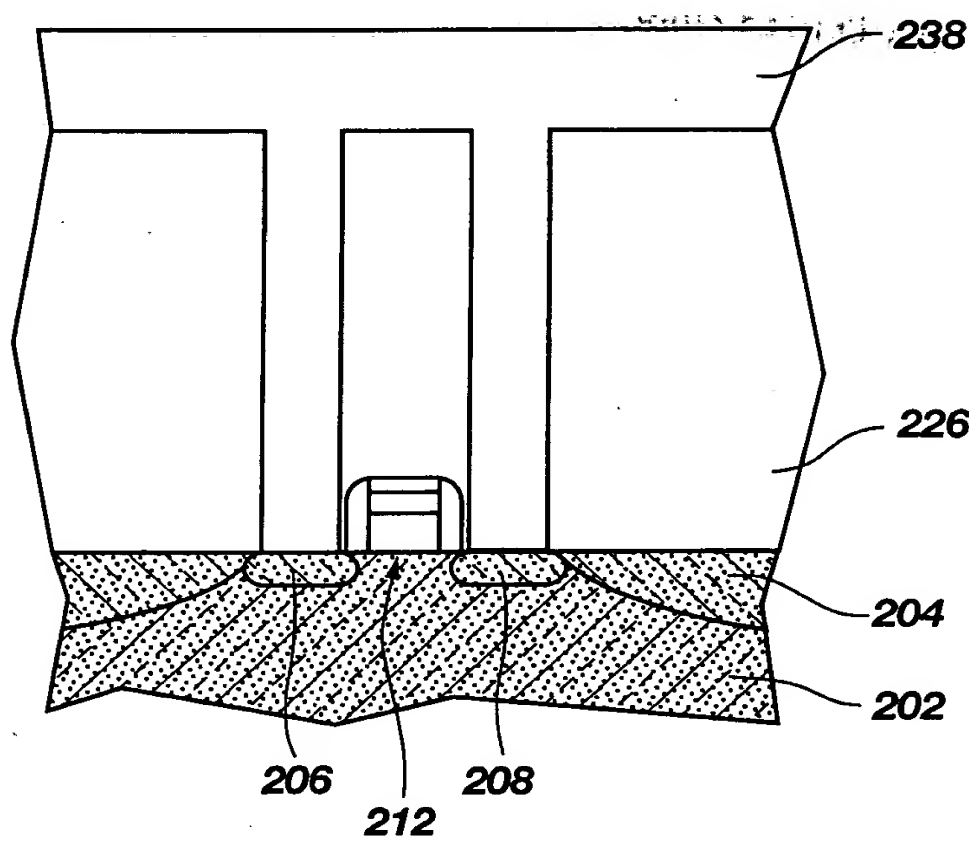
**Fig. 31**  
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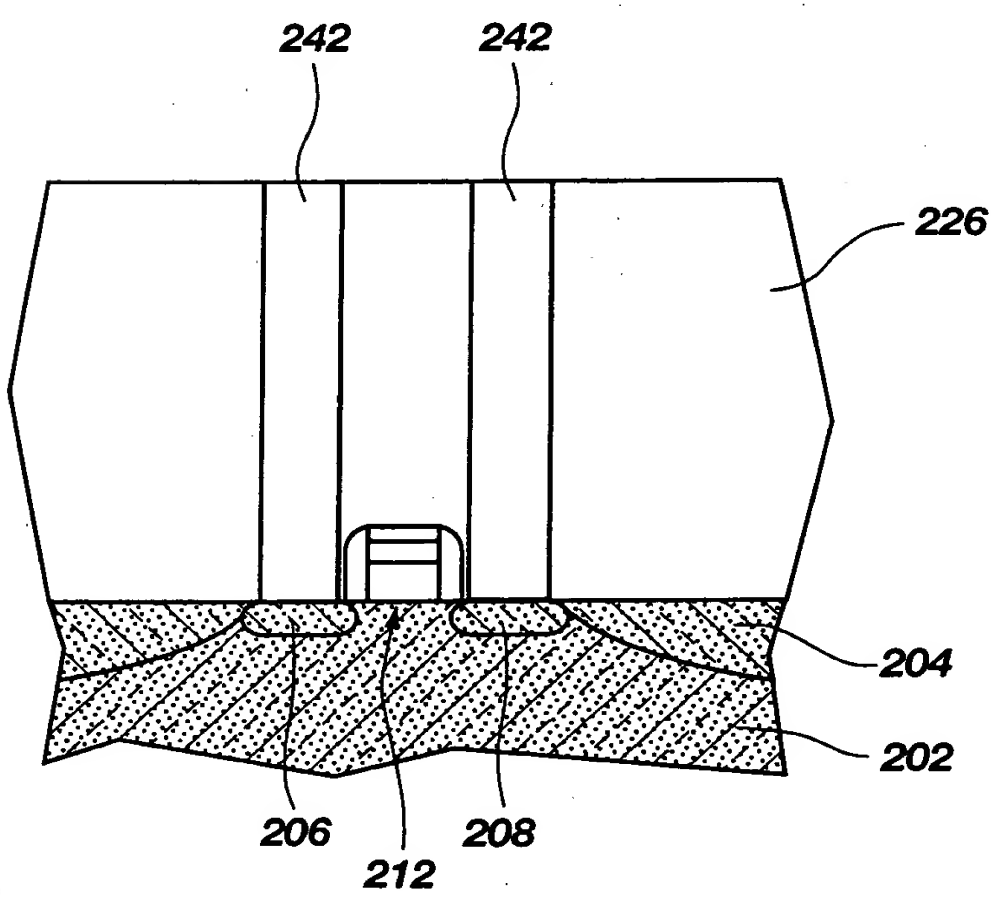
**Fig. 32**  
**(PRIOR ART)**



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**Fig. 33**  
**(PRIOR ART)**

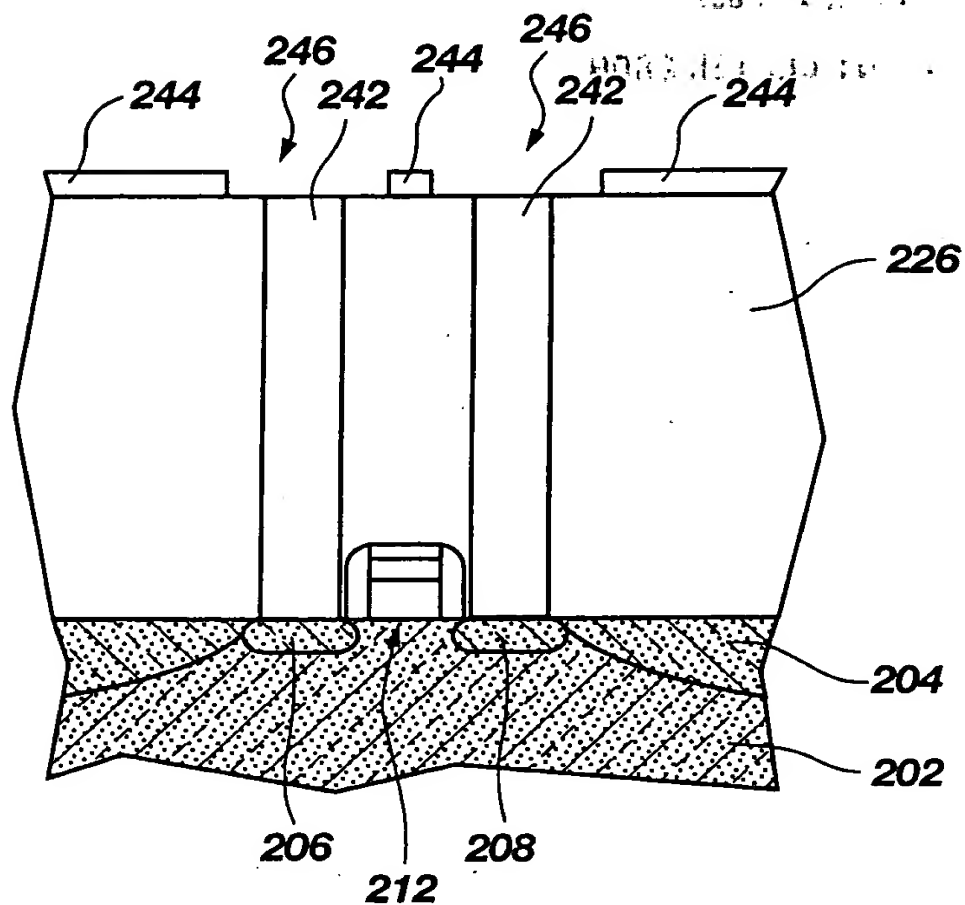


**Fig. 34**  
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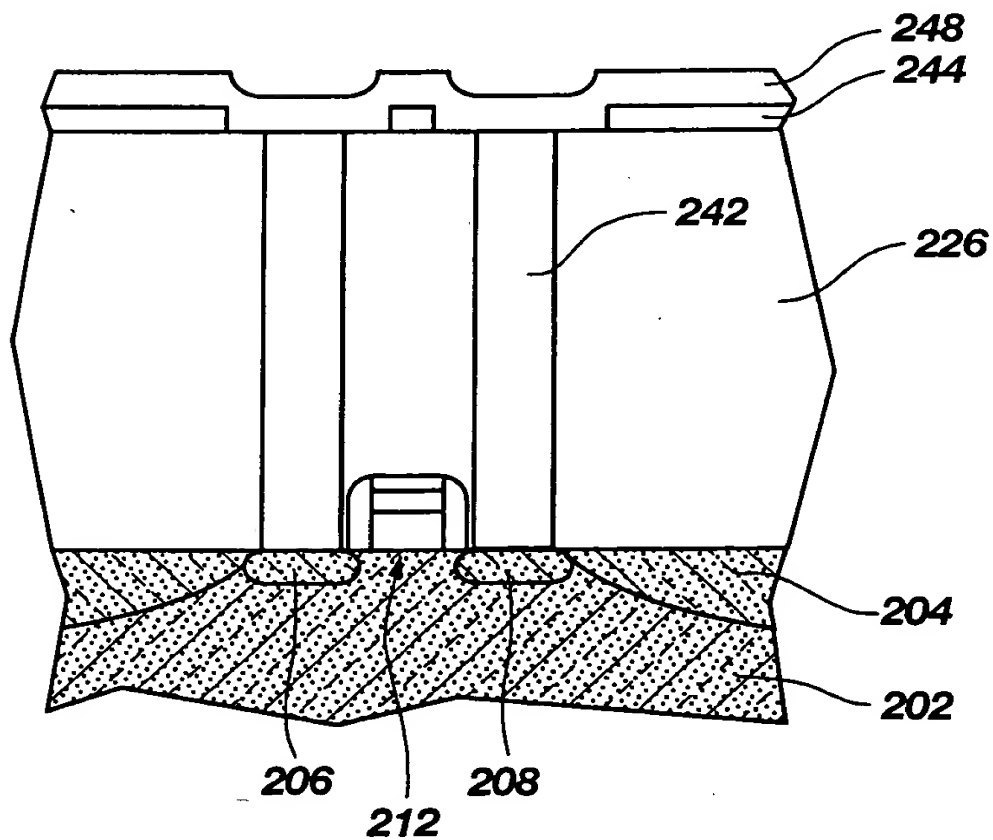




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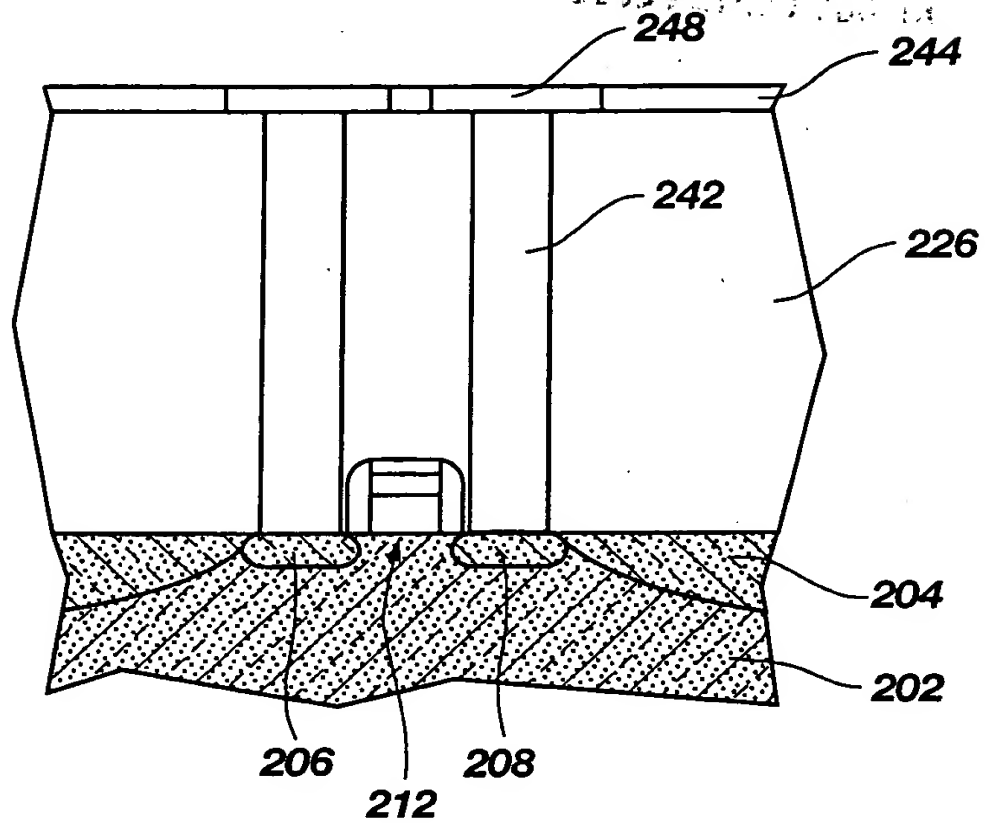
**Fig. 35**  
**(PRIOR ART)**



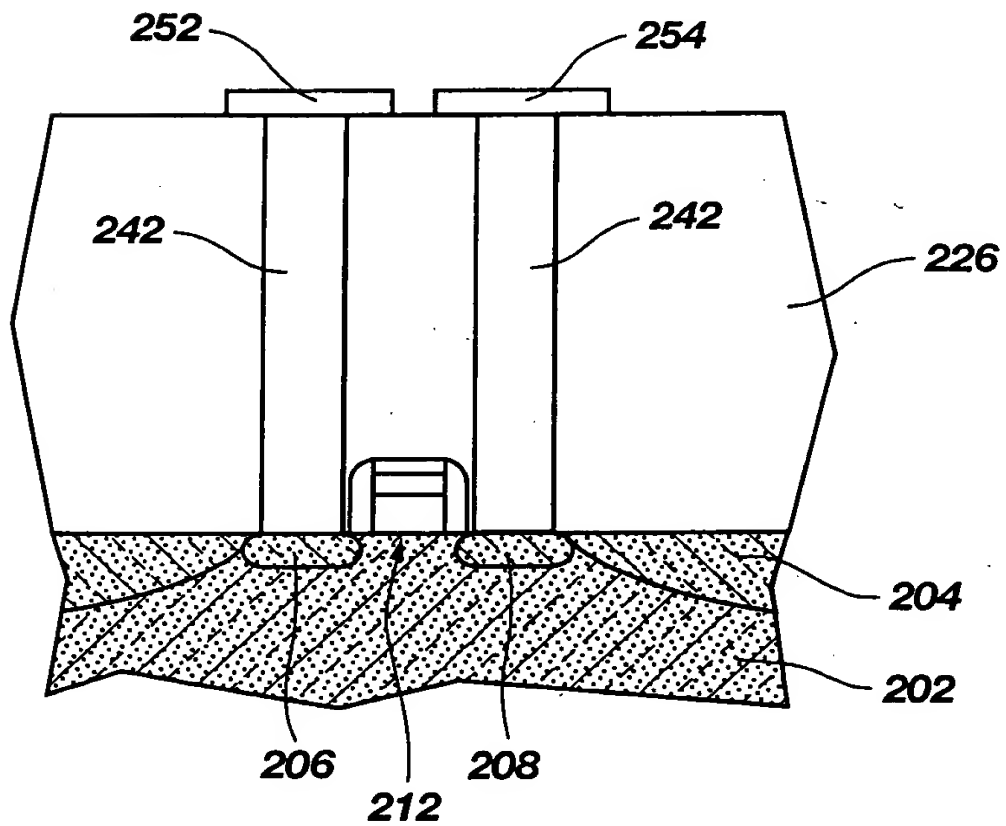
**Fig. 36**  
**(PRIOR ART)**



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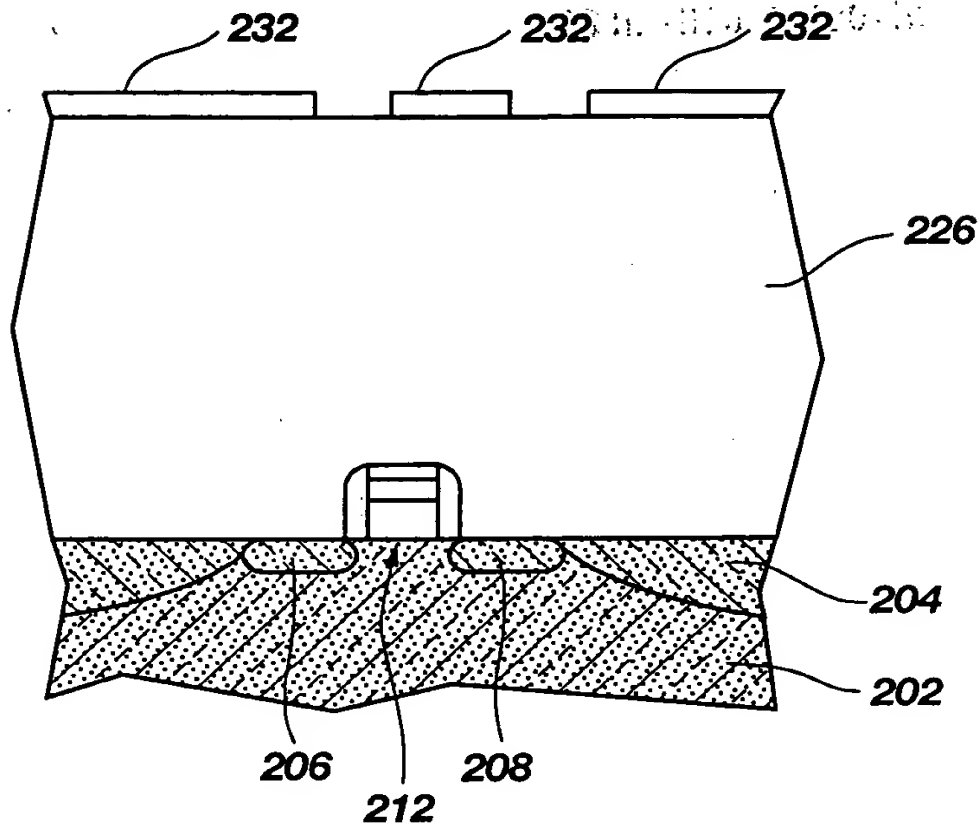
**Fig. 37**  
**(PRIOR ART)**



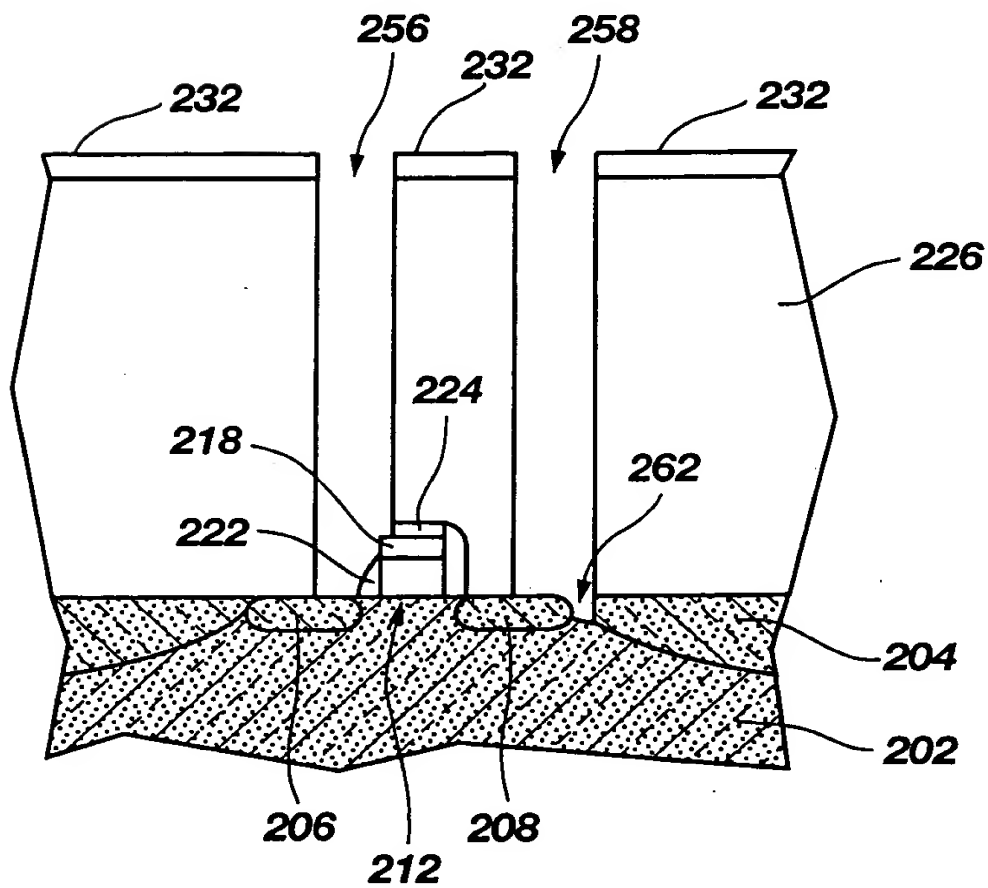
**Fig. 38**  
**(PRIOR ART)**



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**Fig. 39**  
**(PRIOR ART)**



**Fig. 40**  
**(PRIOR ART)**